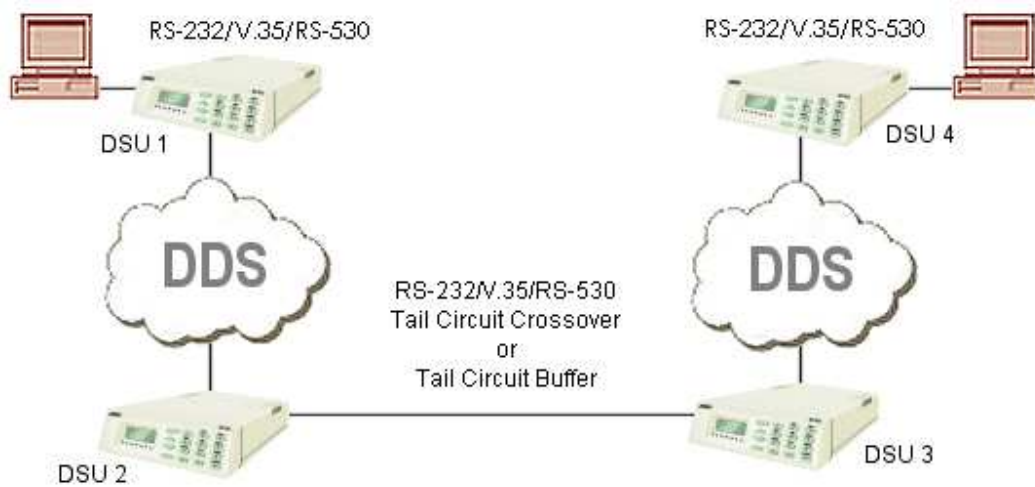




## ADTRAN DSU Tail Circuit Application

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### Typical DSU Tail Circuit Application



Typical DSU Tail Circuit Application

## Tail Circuit Crossover Cables

<b>RS-530 Tail Circuit Crossover</b>	
Pin 7 SGND	Pin 7 SGND
Pin 4 RS	Pin 8 CD
Pin 8 CD	Pin 4 RS
Pin 3 RD-A	Pin 2 SD-A
Pin 16 RD-B	Pin 14 SD-B
Pin 2 SD-A	Pin 3 RD-A
Pin 14 SD-B	Pin 16 RD-B
Pin 17 RC-A	Pin 24 ETC-A
Pin 9 RC-B	Pin 24 ETC-B
Pin 11 ETC-B	Pin 9 RC-B

<b>V.35 Tail Circuit Crossover</b>	
Pin B SGND	Pin B SGND
Pin C RS	Pin F CD
Pin F CD	Pin C RS
Pin R RD-A	Pin P SD-A
Pin T RD-B	Pin S SD-B
Pin P SD-A	Pin R RD-A
Pin S SD-B	Pin T RD-B
Pin V RC-A	Pin U ETC-A
Pin X RC-B	Pin U ETC-A
Pin U ETC-A	Pin V RC-A
Pin U ETC-A	Pin X RC-B

<b>RS-232 Tail Circuit Crossover</b>	
Pin 2 TD	Pin 3 RD
Pin 3 RD	Pin 2 TD
Pin 4 RS	Pin 8 CD
Pin 7 SGND	Pin 7 SGND
Pin 8 CD	Pin 4 RS
Pin 17 RC	Pin 24 ETC
Pin 24 ETC	Pin 17 RC

## Settings

	<b>DSU 1</b>	<b>DSU 2</b>	<b>DSU 3</b>	<b>DSU 4</b>
<b>Loop Rate</b>	The Loop Rates should be set to the provided circuit rate (i.e., 56k, 64k).			
<b>DTE Rate</b>	The DTE Rate and Data Format (i.e., Sync/Async) should match throughout.			
<b>DTE Clock</b>	Normal	Ext	Ext	Normal
<b>Net Clock</b>	Net	Net	Net	Net
<b>Note:</b> If a tail circuit buffer is used instead of a cross-over cable, set all DTE Clocks to Normal.				