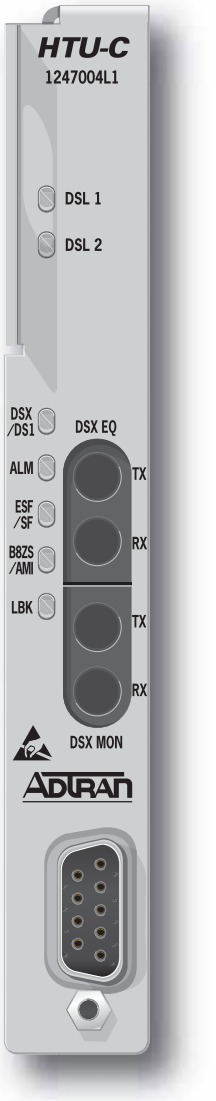

HDSL
3192 HTU-C
Installation and Maintenance Practice

Document Number: 61247004L1-5A
CLEI: T1I3AAPA_ _
April 2008

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Revision History

Revision	Date	Description
A	April 2008	Initial release

Conventions

The following typographical conventions are used in this document:

This font indicates a cross-reference link.

This font indicates screen menus, fields, and parameters.

THIS FONT indicates keyboard keys (ENTER, ESC, ALT). Keys that are to be pressed simultaneously are shown with a plus sign (ALT+X indicates that the ALT key and X key should be pressed at the same time).

This font indicates references to other documentation and is also used for emphasis.

`This font` indicates on-screen messages and prompts.

`This font` indicates text to be typed exactly as shown.

This font indicates silk-screen labels or other system label items.

This font is used for strong emphasis.

NOTE

Notes inform the user of additional, but essential, information or features.

CAUTION

Cautions inform the user of potential damage, malfunction, or disruption to equipment, software, or environment.

WARNING

Warnings inform the user of potential bodily pain, injury, or death.

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Training Fax: 256-963-6217

Training Email: training@adtran.com

Contents

Figures

Tables

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HDSL
3192 HTU-C

INTRODUCTION

This practice is an installation and maintenance guide for the ADTRAN® HDSL 3192 HTU-C (3192 HTU-C). The 3192 HTU-C (P/N 1247004L1) is used to deploy an HDSL T1 circuit using 4-wire metallic facilities. The unit occupies one slot in a 3192 shelf. Figure 1 illustrates the 3192 HTU-C front panel.

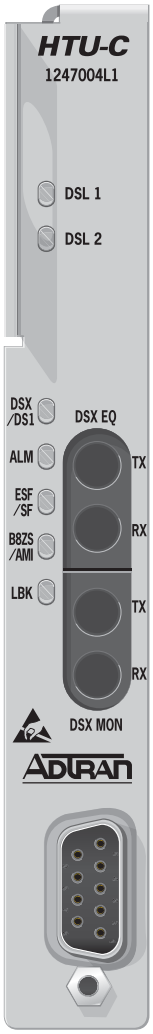

Figure 1. 3192 HTU-C Front Panel

Description

DSX 1 signals are provided to and received from the network while 2B1Q HDSL signals are provided to the local loop. The 3192 HTU-C works in conjunction with the ADTRAN HDSL Transceiver Unit for Remote Office (HTU-R) and HDSL Range Extender (HRE) to provide a DS1 service up to 36,000 feet on the local loop.

This 3192 HTU-C works with multiple list versions of the HTU-R and HRE as listed below.

Table 1. HDSL Compatibility

Part Number	Description
1245024L1	T400 HTU-R with Local Power Option
1246026Lx	6th Gen HTU-R
1247024L1	7th Gen HTU-R, Local Power
1247026L1	7th Gen HTU-R, Span Power
1246041L1	6th Gen T200 HRE
1246045L1	6th Gen 239 HRE
1247041L1	7th Gen T200 HRE
1247045L1	7th Gen 239 HRE

x = any number

The 3192 HTU-C can be deployed in circuits consisting of one HTU-C and one HTU-R. When deployment requires the HRE, the 3192 HTU-C can be deployed with one or two (T200, 239, or 819A) and one HTU-R.

The HDSL local loop operates as two independent subsystems, each operating over a single twisted pair. The 3192 HTU-C communicates over these two twisted pairs to the HTU-R. Each subsystem carries half of the total bandwidth along with a small amount of overhead used for maintenance and performance monitoring.

System power and alarm bus connections are made through the backplane of the 3192 shelf. DSX 1 and HDSL signals are connected through the wire-wrap pins or the 50 or 164-pin shelf connectors related to each individual slot.

The 3192 HTU-C contains an onboard fuse. If this fuse opens, it supplies a –48 VDC voltage to the fuse alarm bus; all front panel indicators turn off. This fuse is not field-replaceable.

The 3192 HTU-C uses a DC-to-DC converter to derive its internal logic and span powering voltages from the –48 VDC office supply. The 3192 HTU-C can span power HREs and HTU-Rs as listed above. The –190 VDC span powering voltage meets all requirements of Class A2 voltages as specified by Bellcore GR-1089-CORE.

Compliance

The 3192 HTU-C is NRTL Listed to the applicable UL safety standards for use in continuous environmental conditions ranging from –40°C to +50°C. Care should be exercised when handling equipment when temperatures at these extremes exist, as surfaces could be very cold or hot.

The 3192 HTU-C meets or exceeds all the applicable requirements of NEBS, Telcordia GR-63-CORE and GR-1089-CORE and is evaluated to ensure proper operational performance is maintained if environmental conditions ranging from –40°C to +65°C are encountered.

The 3192 HTU-C is intended for deployment in Central Office type facilities, EEEs, EECs, and locations where the NEC applies (for example, customer premises). Install the 3192 HTU-C in the appropriate chassis, which is intended to be installed only in Restricted Access Locations by qualified personnel.

This device complies with Part 15 of the FCC rules. Operation is subject to the following two conditions:

1. This device may not cause harmful interference
2. This device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by ADTRAN could void the user's authority to operate this equipment.

The compliance codes for the 3192 HTU-C are listed in Table 2.

Table 2. Compliance Codes

Code	Input	Output
Power Code (PC)	F	C
Telecommunication Code (TC)	–	X
Installation Code (IC)	A	–

WARNING

Voltages up to –200 VDC with respect to ground may be present on the HDSL telecommunications conductors. Voltages up to 200 VDC may be present between individual HDSL telecommunications conductors.

CAUTION

Per GR-1089-CORE the HDSL System is designed and intended for installation as part of a Common Bonding Network (CBN). The HDSL System is not designed nor intended for installation as part of an Isolated Bonding Network (IBN).

CAUTION

Per GR-1089-CORE Section 9, the 3192 HTU-C does not have an internal DC connection between battery return and frame ground. As such, it may be installed in a DC-I (isolated) or DC-C (common) installation. For installations where other cards or the host system have internal connections between battery return and frame ground, the system would be intended for deployment only in a DC-C installation.

NOTE

The HDSL port is classified as Type 1, 3, and 5, as defined in Appendix B of GR-1089-CORE Issue 4, and meets the lightning and power fault criteria with any primary protector that meets any of the voltage limits of GR-974-CORE or GR-1361-CORE (i.e., carbon blocks, gas tubes, solid states, etc.).

NOTE

The DSX-1 port is classified as Type 2 or 4 as defined in Appendix B of GR-1089-CORE Issue 4, and is suitable for connection to intra-building or unexposed wiring or cabling only. Do not metallically connect this port to interfaces which connect to the Outside Plant (OSP) or to the OSP wiring. The DSX-1 port is designed for use as an intra-building interface only (Type 2 or Type 4 ports as described in GR-1089-CORE Issue 4) and requires isolation from exposed OSP cabling. The addition of Primary Protectors is not sufficient protection in order to connect this interface metallically to OSP wiring.

NOTE

Current limiting protectors are not required.

NOTE

The 3192 HTU-C is designed to operate with a nominal operating voltage of –48 VDC and a minimum operating voltage of –40 VDC. The 3192 HTU-C will not be damaged by any steady state voltage below –56.7 VDC.

HDSL DEPLOYMENT GUIDELINES

The ADTRAN HDSL system is designed to provide DS1 based services over loops designed to comply with carrier service area (CSA) guidelines. CSA deployment guidelines are given below.

- All loops are non-loaded only.
- For loops with 26-AWG cable, the maximum loop length including bridged tap lengths is 9 kft.
- For loops with 24-AWG cable, the maximum loop length including bridged tap lengths is 12 kft.
- Any single bridged tap is limited to 2 kft.
- Total bridged tap length is limited to 2.5 kft.
- The total length of multi-gauge cable containing 26-AWG cable must not exceed the following:
  - $12 - \{(3 \times L_{26})/(9 - L_{BTAP})\}$ (in kft)

  Where:
  - L_{26} = Total length of 26-AWG cable excluding bridged taps (in kft)
  - L_{BTAP} = Total length of all bridged taps (in kft)

This deployment criteria is summarized in the chart shown in Figure 2.

Loop loss per kft for other wire is summarized in Table 3.

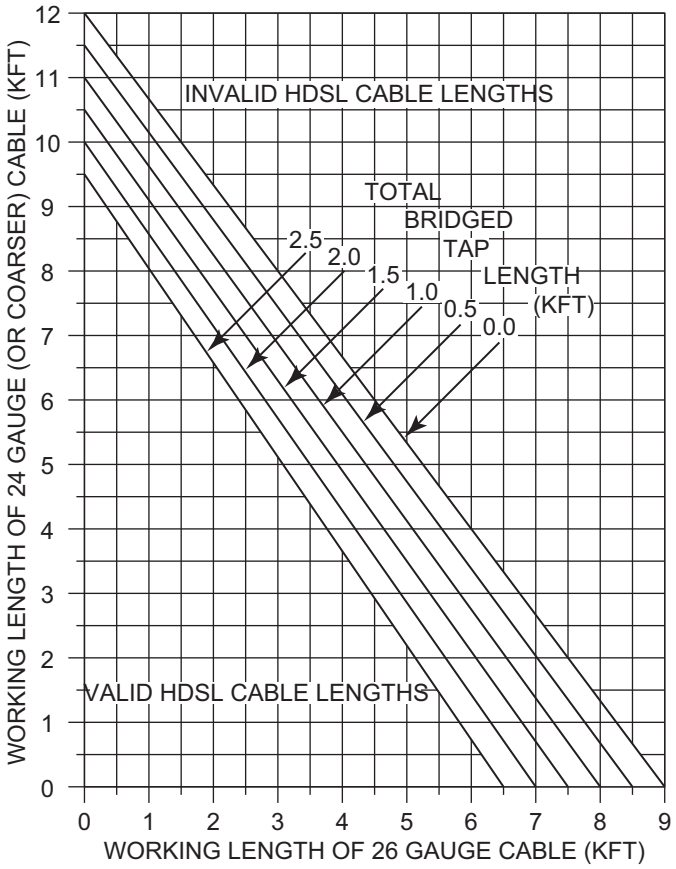

Figure 2. HDSL Deployment Guidelines

Table 3. HDSL Loss Values
(200 kHz cable loss in dB/kft at 135 Ω)

Cable Gauge	Cable Type	Temperature:		
		68°	90°	120°
26	PIC	3.902	4.051	4.253
26	Pulp	4.030	4.179	4.381
24	PIC	2.863	2.957	3.083
24	Pulp	3.159	3.257	3.391
22	PIC	2.198	2.255	2.333
22	Pulp	2.483	2.45	2.629
19	PIC	1.551	1.587	1.634
19	Pulp	1.817	1.856	1.909

Recommended maximum local loop loss information for PIC cable at 70°F, 135 Ω, resistive termination is provided in Table 4.

Table 4. Loop Insertion Loss Data

Frequency (Hz)	Maximum Loss (dB)
3,000	12.0
10,000	15.0
50,000	25.5
100,000	30.0
150,000	32.75
196,000	35.00
200,000	35.25

An approximation for the maximum amount of wideband noise on an HDSL local loop as measured by a 50 kbps filter is ≤ 31 dBrn.

An approximation for the maximum level of impulse noise as measured using a 50 kbps filter on an HDSL loop is ≤ 50 dBrn.

NOTE

These approximations are to be used as guidelines only and may vary slightly on different loops. Adhering to the guidelines should produce performance in excess of 10^{-7} BER.

INSTALLATION

CAUTION

Electrostatic Discharge (ESD) can damage electronic modules. When handling modules, wear an antistatic discharge wrist strap to prevent damage to electronic components. Place modules in antistatic packing material when transporting or storing. When working on modules, always place them on an approved antistatic mat that is electrically grounded.

After unpacking the 3192 HTU-C, inspect it for damage. If damage has occurred, file a claim with the carrier then contact ADTRAN Customer Service. Refer to "Appendix C, Warranty" for further information. If possible, keep the original shipping container for returning the 3192 HTU-C for repair or for verification of shipping damage.

Shipping Contents

The contents include the following items:

- HDSL 3192 HTU-C
- *HDSL 3192 HTU-C Job Aid* (P/N 61247004L1-22)
- *HDSL 3192 HTU-C Compliance Notice* (P/N 61247004L1-17)

Line Build Out Switch

The line build out switch is located on the printed circuit board. Manual configuration should be performed prior to installing the 3192 HTU-C. The available settings are shown in Table 5.

Table 5. Line Build Out Switch Settings

Switch Setting	DIP Switch 1	2	3	Line Length of Cable (in feet)
0	ON	ON	ON	0–133 (default setting)
133	ON	ON	OFF	133–266
266	ON	OFF	ON	266–399
399	ON	OFF	OFF	399–533
533	OFF	ON	ON	533–655

Instructions for Installing the 3192 HTU-C

To install the 3192 HTU-C, perform the following steps:

1. Hold the 3192 HTU-C by the front panel while supporting its bottom edge to engage the chassis edge.
2. Align the 3192 HTU-C edges to fit in the lower and upper guide grooves for the module slot.
3. Slide the 3192 HTU-C into the module slot. Simultaneous thumb pressure at the top (above the **DSL1** LED) and at the bottom (below the electrostatic caution symbol) of the 3192 HTU-C will ensure that it is firmly positioned against the backplane of the chassis.

When the 3192 HTU-C first powers up it runs the power up self-tests. Once the power up self-tests are complete, the status LEDs reflect the true state of the hardware.

Front Panel LEDs

The 3192 HTU-C provides front panel LEDs to display status information. The 3192 HTU-C LEDs and status descriptions are shown in Table 6.

Table 6. Front Panel LEDs

Label	Status		Description
DSL 1/ DSL 2	○	Off	No sync between the HTU-C and HTU-R on Loop 1/Loop 2
	●	Green	Signal quality is good (4 to 9)
	●	Yellow	Signal quality is marginal (1 to 3)
	●	Red	Signal quality is poor (0)
	☼	Flashing	Error at HTU-C or HTU-R; Loop signal quality is indicated by the color of the flashing LED
DSX/ DS1	○	Off	DSX signal is not detected or is of a format that does not match the provisioning of the HDSL circuit
	●	Green	DSX signal is present and synchronized with the HTU-C interface
	☼	Flashing	Bipolar violation (BPV), frame bit error (SF mode) or CRC error (ESF mode) detected on received DSX signal
ALM	○	Off	No alarm conditions exist
	●	Yellow	Remote alarm condition (HTU-R) detected
	●	Red	Alarm condition detected either locally (HTU-C) or both locally and remotely (HTU-C and HTU-R)
ESF/ SF	○	Off	Unit is receiving or provisioned for unframed data
	●	Green	Unit is currently receiving SF data, except when provisioned for unframed data
	●	Yellow	Unit is currently receiving ESF data, except when provisioned for unframed data
B8ZS/ AMI	●	Green	Unit is receiving AMI line code
	●	Yellow	Unit is receiving B8ZS line code
LBK	○	Off	Unit is not armed or in loopback
	●	Yellow	Unit is in loopback toward the network.
	✷	Yellow Flashing	Loopback arming sequence detected and the unit is armed (ready for loopback) but not in loopback

PROVISIONING

Upon initial installation, the 3192 HTU-C is provisioned according to the factory default settings. The provisioning settings can be viewed and manipulated through management access via the front panel RS-232 port. Table 7 lists the available provisioning options and their factory default settings.

Table 7. 3192 HTU-C Provisioning Defaults

Option	Settings	Default
DSX-1 Line Buildout	0-133 feet; 133-266 feet; 266-399 feet; 399-533 feet; 533-655 feet	0-133 feet
DSX-1/DS1 Line Code	B8ZS; AMI	B8ZS
DSX-1/DS1 Framing	Auto; ESF; SF; Unframed; Forced Conversion	Auto
NIU Loopback	Enabled; Disabled	Enabled
New England 1:6 LPBK	Disabled; Enabled	Disabled
Loopback Timeout	None; 60 Min.; 120 Min.	120 Min.
Customer Loss Response	AIS; CDI; Loopback	AIS
Latching Loopback Mode	T1; FTI	T1
PRM Mode	None; NPRM; SPRM	None
DS1 TX Level	0 db; –15 db	0 db
HTUC Shelf Alarm	Enabled; Disabled	Disabled
Span Power	Enabled; Disabled	Enabled
DS0 Blocking	None blocked; Any of 01-24 blocked or unblocked	None blocked

CONNECTIONS

The 3192 HTU-C occupies one card slot in a CI Wescom 3192 Office Repeater Bay. Power and alarm signals are provided to the card through the backplane of the shelf. DSX-1 and HDSL loop signals are connected to the wire-wrap pins or mass termination shelf connectors corresponding to the slot the unit occupies. Connections to the DSX pins are intended for intra-building wiring only. Refer to Figure 3 for 3192 HTU-C edge connector wiring.

The 3192 HTU-C is capable of span powering the HTU-R by applying simplex current to the local loop. From 10 to 125 mA of current is coupled onto the HDSL span to power the HTU-R and HRE when deployed.

HDSL Loops 1 and 2 are dual duplex loops (capable of transmitting and receiving data simultaneously).

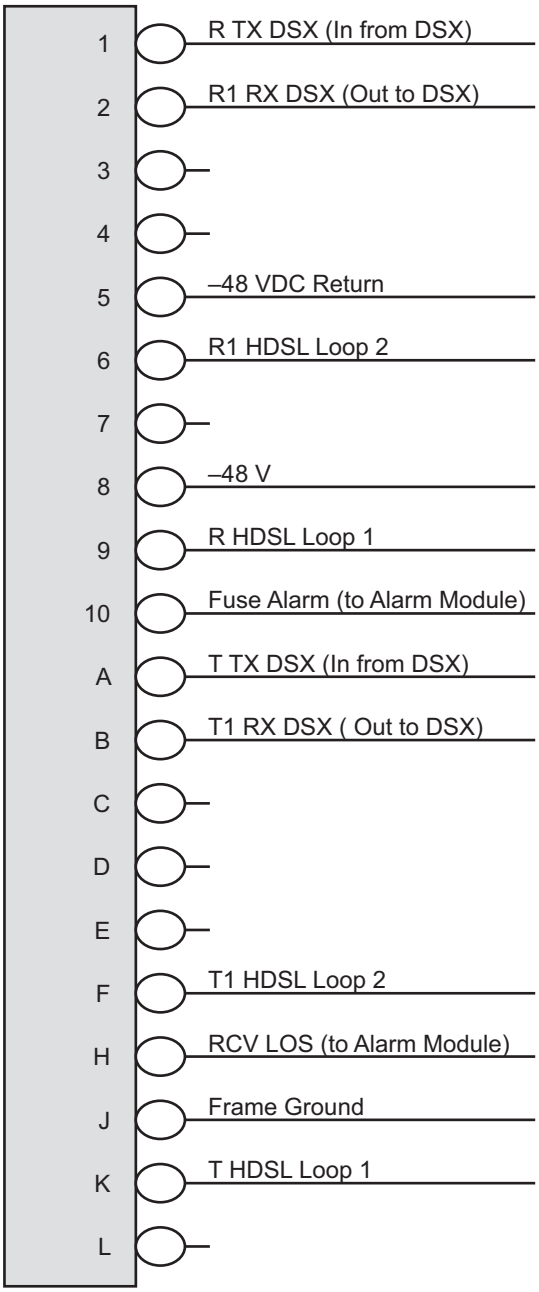

Figure 3. 3192 HTU-C Edge Connector Wiring

The span powering voltage is –190 volts with Loop 1 providing the negative voltage and Loop 2 the return (see Figure 4).

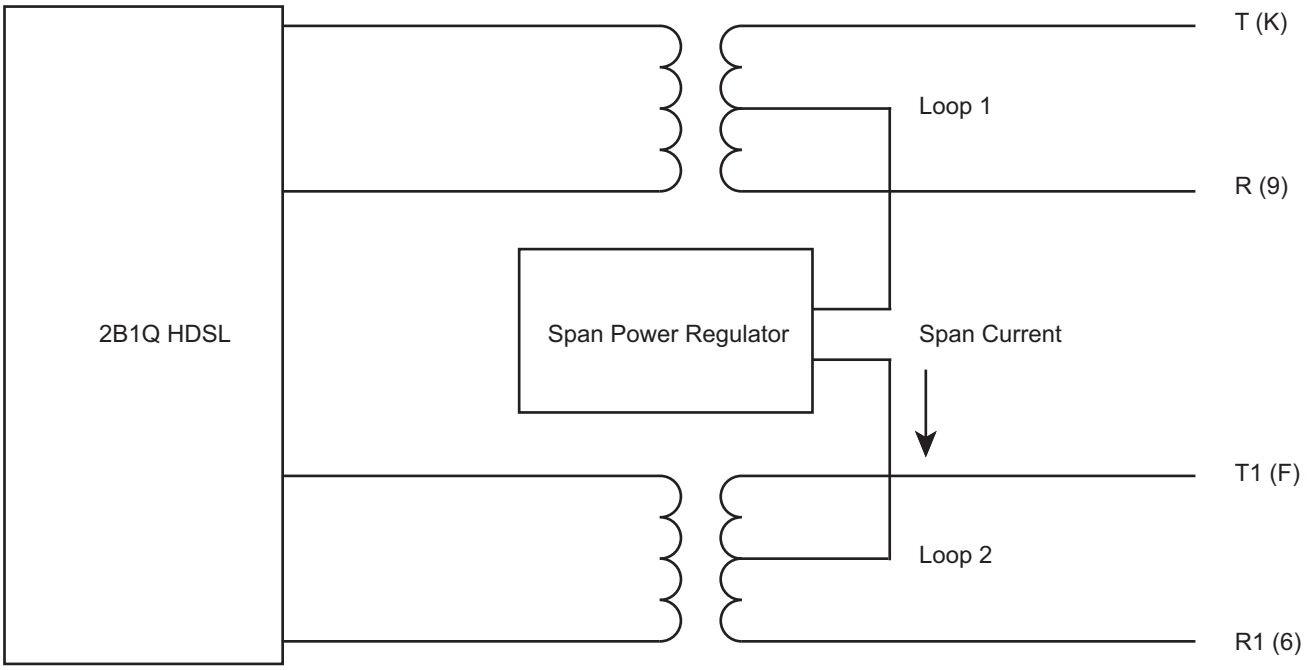

Figure 4. 3192 HTU-C Span Power Diagram

Alarm Connections

The following alarm signals are connected to the 3192 Alarm Module.

Table 8. 3192 HTU-C Alarm Connections

Pin	Label	Function
H	RCV LOS	Loss of Signal Alarm
10	FUSE ALARM	Fuse Alarm of –48V Supply

Alarm processing is actually performed by the 3192 Alarm Module. Refer to the 3192 Alarm Module documentation for further information.

Alarm conditions are not reported at the 3192 HTU-C until the HDSL circuit is terminated by connecting an HTU-R. This allows circuit pre-provisioning. Once the 3192 HTU-C is terminated with an HTU-R, the 3192 HTU-C goes into an in-service state.

CONTROL PORT OPERATION

The 3192 HTU-C provides a faceplate-mounted DB9 connector that supplies an RS-232 interface for connection to a controlling terminal. The pinout of the DB9 is illustrated in Figure 5.

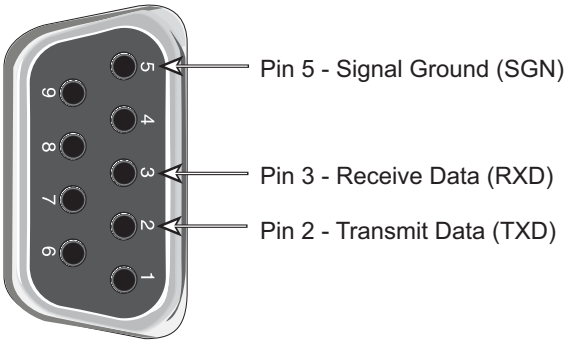

Figure 5. RS-232 (DB9) Pin Assignments

The terminal interface operates at data rates from 1.2 kbps to 19.2 kbps. The asynchronous data format is fixed at 8 data bits, no parity, and 1 stop bit. The supported terminal type is VT-100 or compatible.

NOTE

If using a personal computer (PC) with terminal emulation capability, be sure to disable any power saving programs. Otherwise, communication between the PC and the HDSL unit may be disrupted, resulting in misplaced characters or screen time-outs.

USER INTERFACE

This section provides detailed information on the following:

Menu Structure

The menu structure for the 3192 HTU-C is a layered menu tree. Each layer of the menu tree is displayed as a menu or a screen.

Menus

A menu is a display that provides numbered selections that are used to navigate to related menus, modify provisioning information, or display information screens. A menu can contain the following objects:

- Menu Option: A menu option is indicated by a number which, when selected, navigates the display to another menu layer or is used to change the option setting.
- Read-only Field: A read-only field displays information that cannot be changed. The information displayed in a read-only field can be static or can be automatically updated by the unit.
- Read-write Field: A read-write field displays information that, when selected, can be modified.
- Hot Key: A hot key is a key or combination of keys that are assigned to a function. Hot keys are indicated by the required key(s) and a brief description (that is, “?” - Help).

Screen

A screen is a display that usually indicates the end of a menu tree path. A screen can contain the following objects:

- Read-only Field: A read-only field displays information that cannot be changed. The information displayed in a read-only field can be static or can be automatically updated by the unit.
- Read-write Field: A read-write field displays information that, when selected, can be modified.
- Hot Key: A hot key is a key or combination of keys that are assigned to a function. Hot keys are indicated by the required key(s) and a brief description (that is, “?” - Help).

Menu Navigation

Basic menu navigation is accomplished by selecting the desired option number and then pressing ENTER. To return to the previous menu, press the ESC (escape) key. To access the System Help screen, press the question mark (?) key, and press ENTER.

Screen Abbreviations

Table 9 lists the abbreviations used in the screen examples shown in Figures 6 through 18.

Table 9. Screen Abbreviations

Abbreviation	Definition
ES	Errored Seconds • DSX/DS1 – SF: Second in which a BPV or frame bit error occurs – ESF: Second in which a BPV or CRC error occurs • HDSL – Second in which a CRC error occurs
SES	Severely Errored Seconds. • DSX/DS1 – SF: Second in which 1544 BPVs or 8 frame bit errors occurs – ESF: Second in which 1544 BPVs or 320 CRC errors occur • HDSL – Second in which 165 CRC errors occurs
UAS	Unavailable Seconds • DSX/DS1 – Second in which there is a loss of signal or sync • HDSL – Second in which there is a loss of signal or sync
SF	Superframe Format
ESF	Extended Superframe Format
B8ZS	Binary 8 Zero Substitution
AMI	Alternate Mark Inversion
LBO	Line Build Out
BPV	Bipolar Violation • DSX/DS1 – Second in which a bipolar violation occurs
NIU	T1 Network Interface Unit
S/N	Serial number
15M	Fifteen-Minute Period
24H	Twenty-Four-Hour Period

MENU DESCRIPTIONS

The following subsections describe the 3192 HTU-C menu screens. A terminal session is initiated by entering multiple space bar characters, which determines the speed of the terminal. Once the speed has been determined, an Introductory menu appears. This screen is illustrated in Figure 6.

NOTE

The screens illustrated in Figures 6 through 18 apply to an HDSL circuit deployed with ADTRAN's HDSL technology, using an 3192 HTU-C, an HTU-R, and two HREs. This sample configuration was chosen in order to illustrate as much functionality as possible; however, other configurations are possible and their displays will vary slightly from those shown in this section.

```
Circuit ID:HNTSVLALHDSL                                    MM/DD/YY hh:mm:ss
                                    ADTRAN
                                901 Explorer Boulevard
                              Huntsville, Alabama 35806-2807

      --------------- For Information or Technical Support----------------
       Support Hours ( Normal 7am - 7pm CST, Emergency 7 days x 24 hours )
  Phone: 800.726.8663 / 888.873.HDSL Fax: 256.963.6217 Internet: www.adtran.com
 ------------------------------------------------------------------------------
  HTU-C INFORMATION                       HTU-R INFORMATION
  -----------------                       -----------------
  S/N : A601A4121                         S/N : A106B5672
  CLEI: T1I3AAPAAA                        CLEI: T1I3AAVAAB
  MANF: 01/08                             MANF: 01/08

  HRE #1 INFORMATION                      HRE #2 INFORMATION
  ------------------                      -------------------
  S/N : B905A5505                         S/N : A917C2045
  CLEI: T1RPAACBAA                        CLEI: T1RPAACBAA
  MANF: 01/08                             MANF: 01/08


                          Press "M" to view Main Menu.
```

Figure 6. Introductory Menu Screen

HDSL Main Menu

From the Introductory menu, select the ADTRAN HDSL Main Menu by pressing "M." Various Operation, Administrative, Maintenance, and Provisioning (OAM&P) screens may be accessed from the Main Menu (Figure 7).

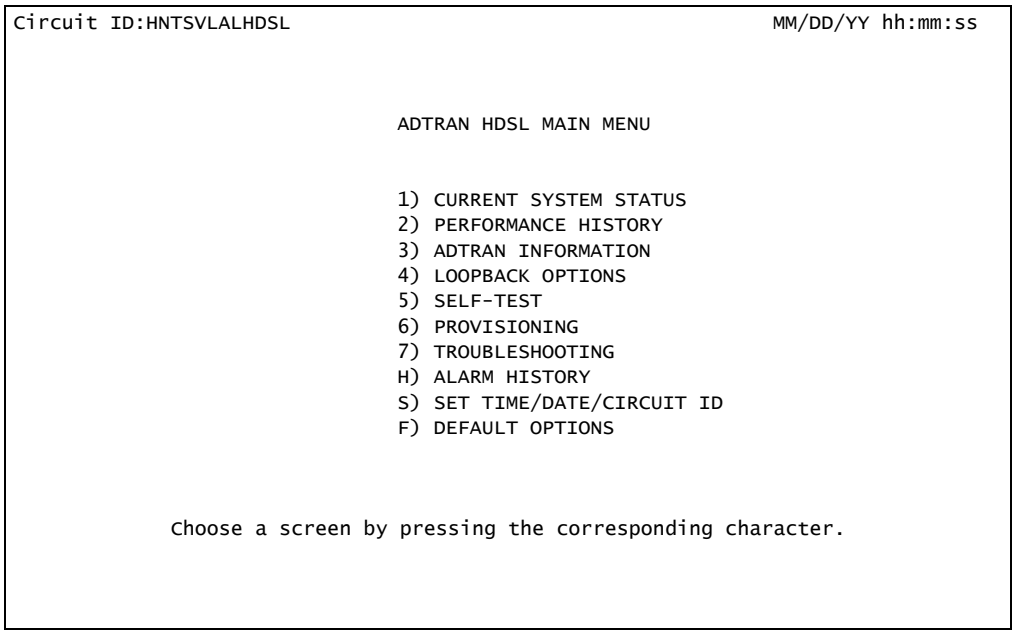

Figure 7. HDSL Main Menu

A list of options for the HDSL Main Menu and their descriptions can be found in Table 10.

Table 10. HDSL Main Menu Options

Option	Description	Function
1	Current System Status	This option displays the "Current System Status Screen" on page 19.
2	Performance History	This option displays the "Performance History Screen" on page 23.
3	ADTRAN Information	This option displays the "Introductory Menu Screen" on page 16.
4	Loopback Options	This option displays the "Loopback Options Screen" on page 25.
5	Self-Test	This option displays the "Self-Test Options Screen" on page 26.
6	Provisioning	This option displays the "Provisioning Screen" on page 26.
7	Troubleshooting	This option displays the "Troubleshooting Display Screen" on page 27.

Table 10. HDSL Main Menu Options (Continued)

Option	Description	Function
H	Alarm History	This option displays the "Alarm History Screen" on page 28.
S	Set Time/Date/Circuit ID	This option displays the "Set Time/Date/Circuit ID Screen" on page 29.
F	Default Options	This option displays the "Default Options Screen" on page 30.

Current System Status Screens

The Current System Status screen, illustrated in Figure 8, provides quick access to status information for both the 3192 HTU-C and the HTU-R.

```
Circuit ID:HNTSVLALHDSL                                        MM/DD/YY hh:mm:ss
LOOP #1 <NETWORK> LOOP #2   CURRENT SYSTEM STATUS     LOOP #1 <CUSTOMER> LOOP #2
----------HTU-C----------                              ----------HTU-R----------
29(29) dB      29(29) dB    <-  LOSS (CUR MAX)  ->    29(29) dB        30(30) dB
YES            YES          <-  SYNC            ->       YES              YES
000/00000      000/00000    <-  ES    15M/24H   ->    000/00000        000/00000
000/00000      000/00000    <-  SES   15M/24H   ->    000/00000        000/00000
000/00000      000/00000    <-  UAS   15M/24H   ->    000/00000        000/00000
   LOOPBACKS INACTIVE                                    LOOPBACKS INACTIVE
   HTU-C Margin (dB)      DSX-1                  DS1     HTU-C Margin (dB)
      CUR/MIN/MAX         --------------------------        CUR/MIN/MAX
                          ESF   <-  FRAME  ->   ESF
                          B8ZS  <-  CODE   ->   B8ZS
   LP1 00/00/00           0-133 <-  LBO    ->   0 dB      LP1 00/00/00
   LP2 00/00/00           N/A   <-  NIU    ->   NO        LP2 00/00/00
                          00000 <-  BPV    ->   00000
                          00000 <-  ES     ->   00000
                          00000 <-  SES    ->   00000
                          00000 <-  UAS    ->   00000
                          NONE  <- ALARMS  ->   NONE

     Press “Z” to zero registers, “X” to restart MIN/MAX, “M” for Main Menu,
                   “H” for HDSL Range Extender #1 (HRE) View.
```

Figure 8. Current System Status Screen

Press “Z” at the Current System Status screen in order to reset the current performance registers to zero on both the Current System Status and Performance History screens. A prompt requires user confirmation to execute the zero registers function.

The signal quality meters also display MIN and MAX labels to indicate historical signal quality performance. The MIN and MAX registers can be reset by pressing “X.”

Figures 8 and 9 consolidate current information for the HDSL, DSX-1, and DS1 interfaces. A key to the information provided is found in the center of the screen and defined in Table 11 below. Arrows indicate the key applies to both the 3192 HTU-C and HTU-R.

Table 11. HDSL, DS1, and DSX-1 Key Definitions

Indicator	Definition
LOSS	Pulse attenuation measurement (1)
SYNC	HDSL loop 1 and loop 2 sync status
ES 15M/24H	Errored seconds (2)
SES 15M/24H	Severely errored seconds (2)
UAS 15M/24H	Unavailable seconds (2)

1. LOSS is typically several dB less than the insertion loss measured at 200 kHz. The LOSS measurement is a better indication of the loop's attenuation of the 2B1Q signal than the insertion loss measured at a single frequency. ADTRAN HDSL can operate on cables with an excess of 30 dB LOSS.
2. The first number is for the current 15-minute period and the second is the current 24-hour period (Loop 1 and Loop 2 numbers are displayed).

An indication of Pair Reversal (if present) appears at the bottom of the first key column. Definitions for this key are provided in Table 12. Status and configuration information for the DS1 and DSX-1 signals is located in the center of the screen near the bottom.

Table 12. Pair Reversal Key Definitions

Indicator	Definition
FRAME	T1 framing format selected
CODE	T1 line coded selected
LBO	Line build-out selected (for DSX-1), Customer signal of 0 or –15 dB (for DS1)
NIU	Network interface unit enabled
BPV	Bipolar violations detected (DSX-1 and DS1)
ES	Errored seconds (DSX-1 and DS1)
SES	Severely errored seconds (DSX-1 and DS1)
UAS	Unavailable seconds (DSX-1 and DS1)
Alarms	Lists current alarm condition status

A measure of signal quality for each HDSL loop appears on the bottom right and left of the screen. Guidelines for interpreting the measure indicators are given in Table 13.

Table 13. HDSL Loop Signal Quality

Measure	Signal Quality	Noise Margin
0	Poor	≤ 0 dB (≈10^{-7} BER)
1-3	Marginal	above 10^{-7} BER in dB
>4	Excellent	≥ 4 dB above 10^{-7} BER

Predicting performance based upon signal quality varies with each loop. Generally, a noise margin of 0 or higher supports a bit error rate or better the 10^{-7}. ADTRAN defined guidelines that correspond to the operation of the 3192 HTU-C faceplate LEDs labeled LP1 and LP2 are listed in Table 14.

Table 14. ADTRAN Noise Margin Guidelines

Margin	LP1/LP2 LED	Loop Quality
Margin = 0	Red	Poor Loop Quality
0 < Margin < 4	Yellow	Marginal Loop Quality
Margin > 4	Green	Good Loop Quality

Current System Status - HRE Screen

From the Current System Status screen, press H once to view current system status for HRE#1. Press “H” a second time to view current system status for HRE#2. See Figure 9 for the HRE Current System Status screen.

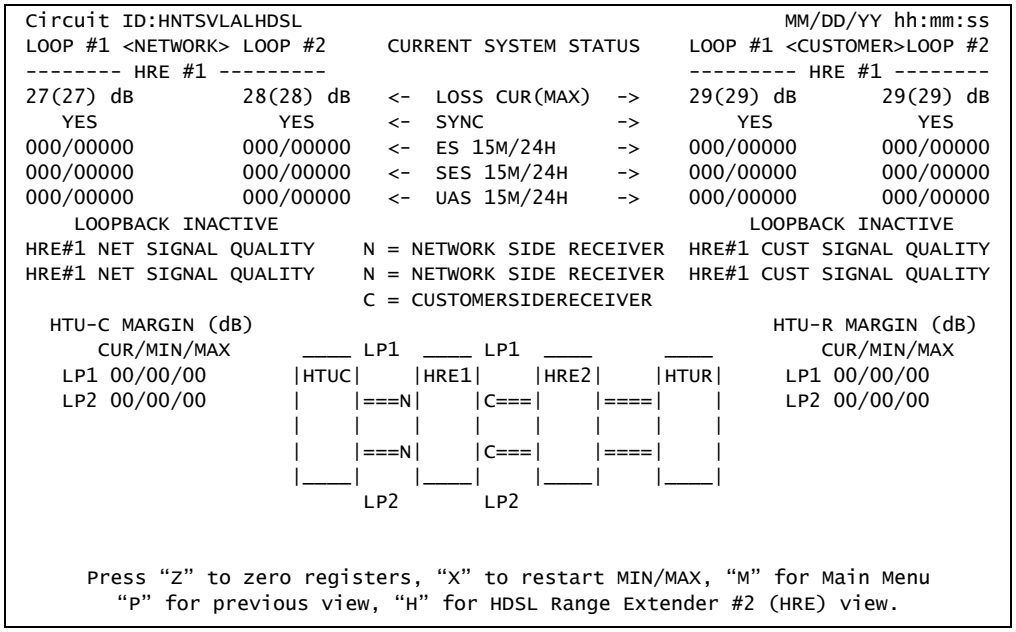

Figure 9. Current System Status - HRE Screen

Performance History Screen

The Performance History screen (Figure 10) displays the historical HDSL and T1 performance data in several different registers. At each 15-minute interval, the performance information is transferred to the previous 15-minute performance data register. This 3192 HTU-C stores performance data in 15-minute increments for the last 24-hour period. At each 24-hour interval, the performance data is transferred into the 24-hour performance data register also accessed from the Performance History screen.

```
Circuit ID:HNTSVLALHDSL                                        MM/DD/YY hh:mm:ss
      24 HOUR REGISTERS       PERFORMANCE HISTORY    15 MINUTE REGISTERS
     ---ES---SES---UAS---                        ---ES--SES-UAS-------ES--SES-UAS-
      00000 00000 00000     <---CURRENT--->         000 000 000
12/31 ----- ----- -----     <--          -->  14:30 000 000 898 10:30 000 000 000
12/30 ----- ----- -----       |          |    14:15 000 000 001 10:15 000 000 000
12/29 ----- ----- -----       |          |    14:00 000 000 006 10:00 000 000 000
12/28 ----- ----- -----       |          |    13:45 000 000 000 9:45  000 000 000
12/27 ----- ----- -----       |PREVIOUS  |    13:30 000 000 000 9:30  000 000 000
12/26 ----- ----- -----       |          |    13:15 000 000 000 9:15  000 000 000
12/25 ----- ----- -----     <--          |    13:00 000 000 000 9:00  000 000 000
                                         |    12:45 000 000 000 8:45  000 000 000
VIEW 1 : HTU-C DSX-1                     |    12:30 000 000 000 8:30  000 000 000
                                         |    12:15 000 000 000 8:15  000 000 000
1->|H|-3---|H|-----|H|---4-|H|-->       |    12:00 000 000 000 8:00  000 000 000
   |T|     |R|     |R|     |T|          |    11:45 000 000 000 7:45  000 000 000
   |U|     |E|     |E|     |U|          |    11:30 000 000 000 7:30  000 000 000
<--|C|-5---|1|-----|2|---6-|R|<-2       |    11:15 000 000 000 7:15  000 000 000
                                         |    11:00 000 000 000 7:00  000 000 000
Press view number to select view         -->  10:45 000 000 000 6:45  000 000 000
Press "H" to view HRE #1 history
                                                Press "B" to go back 8 hours
                        Press "M" to return to the Main Menu
```

Figure 10. Performance History Screen

Performance History - HRE Screen

From the Performance History screen, press “H” once to view the Performance History screen for HRE#1. Press “H” a second time to view the Performance History screen for HRE#2. The Performance History-HRE screen is illustrated in Figure 11.

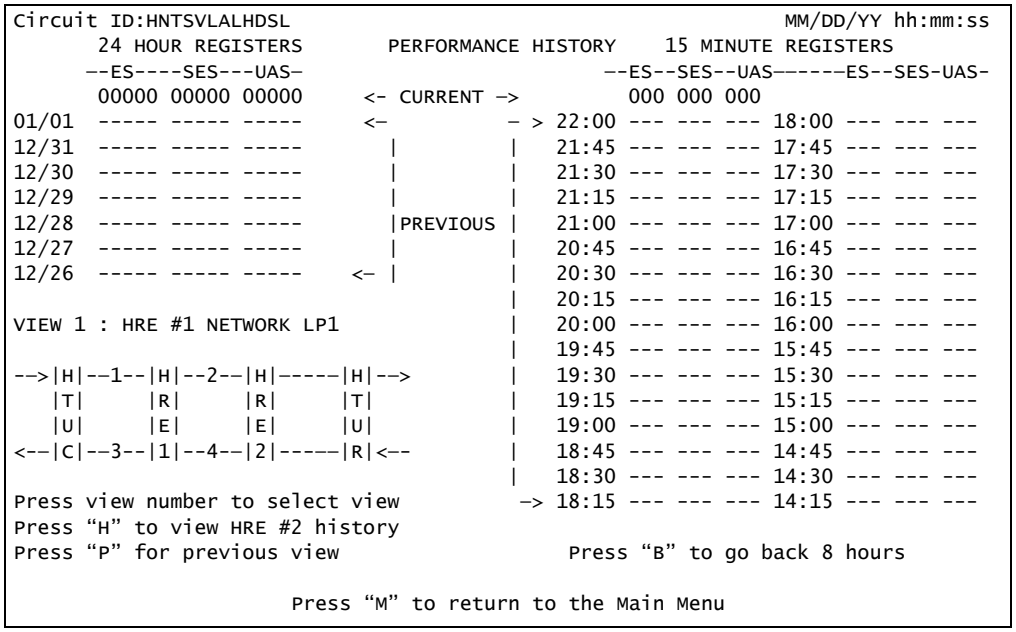

Figure 11. Performance History - HRE Screen

Loopbacks Options Menu

The Loopback Options menu, illustrated in Figure 12, displays and allows the changing of loopback settings throughout the HDSL circuit.

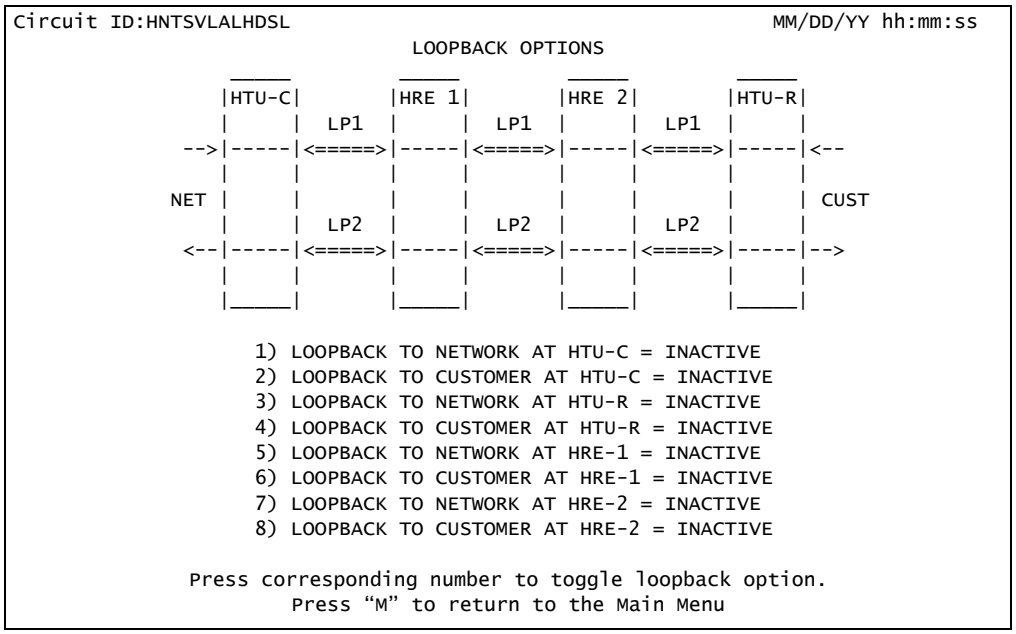

Figure 12. Loopback Options Screen

Self Test Options Screen

The Self Test Option screens, illustrated in Figure 13, allows the initiation of self tests of the 3192 HTU-C and HTU-R by pressing “S.”

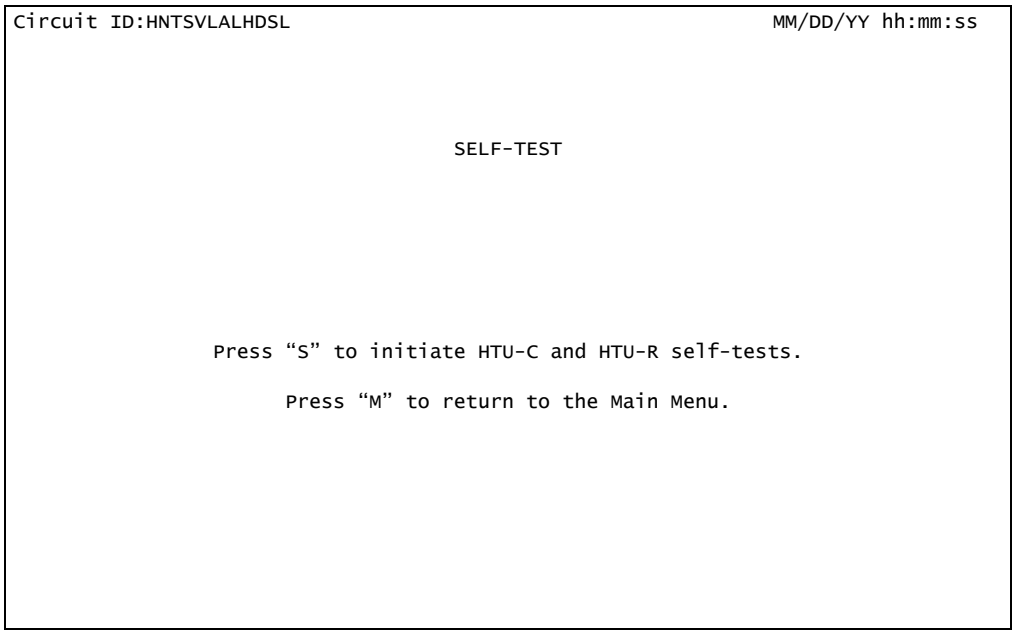

Figure 13. Self-Test Options Screen

Provisioning Screen

The Provisioning screen, (Figure 14), provides the option to change the 3192 HTU-C provisioning settings. A full list of provisioning options can be found in Table 7 on page 10.

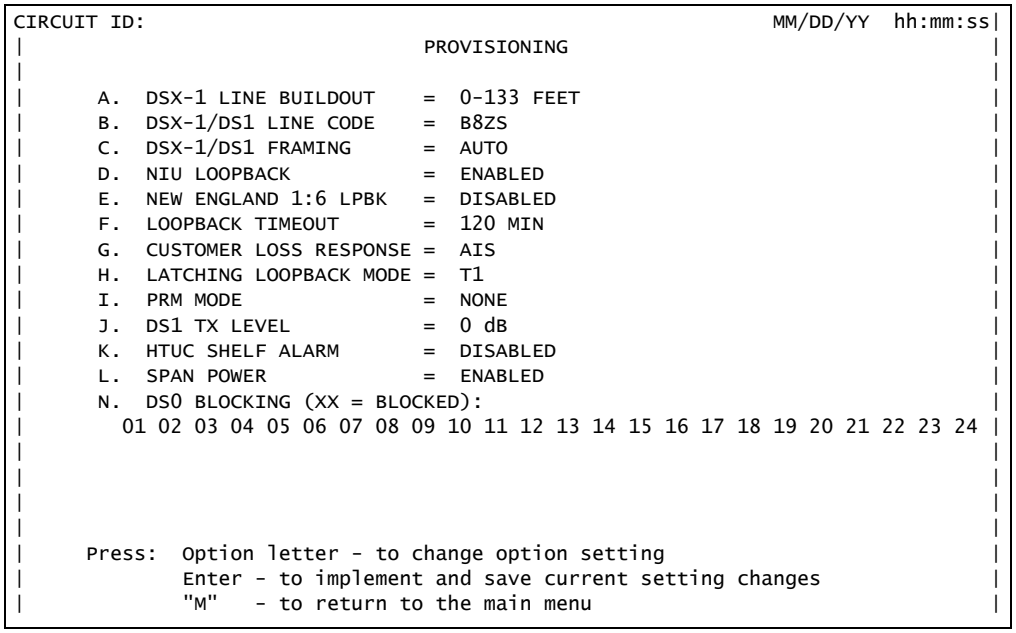

Figure 14. Provisioning Screen

Troubleshooting Display Screen

The Troubleshooting Display screen, illustrated in Figure 15, graphically depicts an HDSL circuit. The 3192 HTU-C reviews red, yellow, and blue alarm conditions in the circuit to automatically predict where a fault is located. Once a fault location is suspected, the corresponding portion of the circuit on the screen is highlighted and a message describing the failure appears.

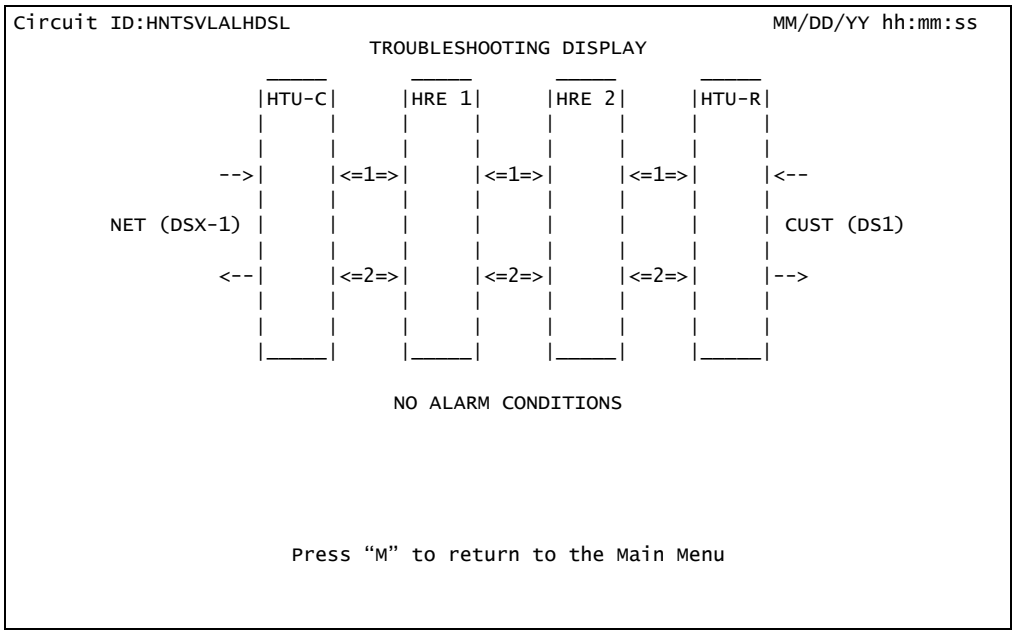

Figure 15. Troubleshooting Display Screen

Alarm History Screen

The Alarm History screen illustrated in Figure 16 provides detailed information on the alarm history of the HDSL and T1 spans. Information provided includes alarm location, type, first and last time/date, current status, and count.

```
Circuit ID:HNTSVLALHDSL                                    MM/DD/YY hh:mm:ss
                                T1 Alarm History
 LOCATION      ALARM        FIRST              LAST             CURRENT   COUNT
 ------------------------------------------------------------------------------
 HTU-C         RED(LOS)                                         OK          000
  (DSX-1)      YELLOW                                           OK          000
               BLUE(AIS)                                        OK          000
 HTU-R         RED(LOS)                                         OK          000
 (DS1)         YELLOW                                           OK          000
               BLUE(AIS)                                        OK          000
                                HDSL Span History
 ------------------------------------------------------------------------------
 SPAN 1        LP1 HLOS                                         OK          000
               LP2 HLOS                                         OK          000
 HTU-C         LP1 MRGN                                         OK          000
               LP2 MRGN                                         OK          000
 HRE-1         LP1 MRGN                                         OK          000
               LP2 MRGN                                         OK          000


      Press: C to clear history : H to scroll span alarms : M for main menu
```

Figure 16. Alarm History Screen

Set Time/Date/Circuit ID Menu

The Set Time/Date/Circuit ID menu, illustrated in Figure 17, provides additional provisioning options. Enter the time parameters as military time (for example, enter 3:15 p.m. as 15:15:00). Enter the date parameters in MM/DD/YY format. Enter the Circuit ID as a 25–character alphanumeric string.

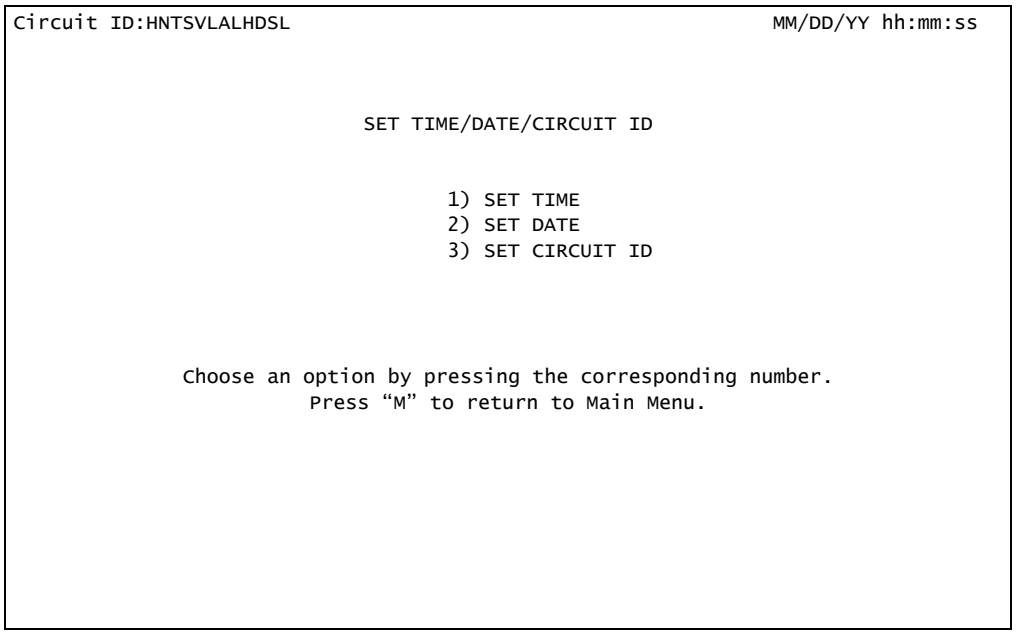

Figure 17. Set Time/Date/Circuit ID Screen

Default Options Screen

The Default Options screen, illustrated in Figure 18 allows the setting of all provisioning options to the factory defaults.

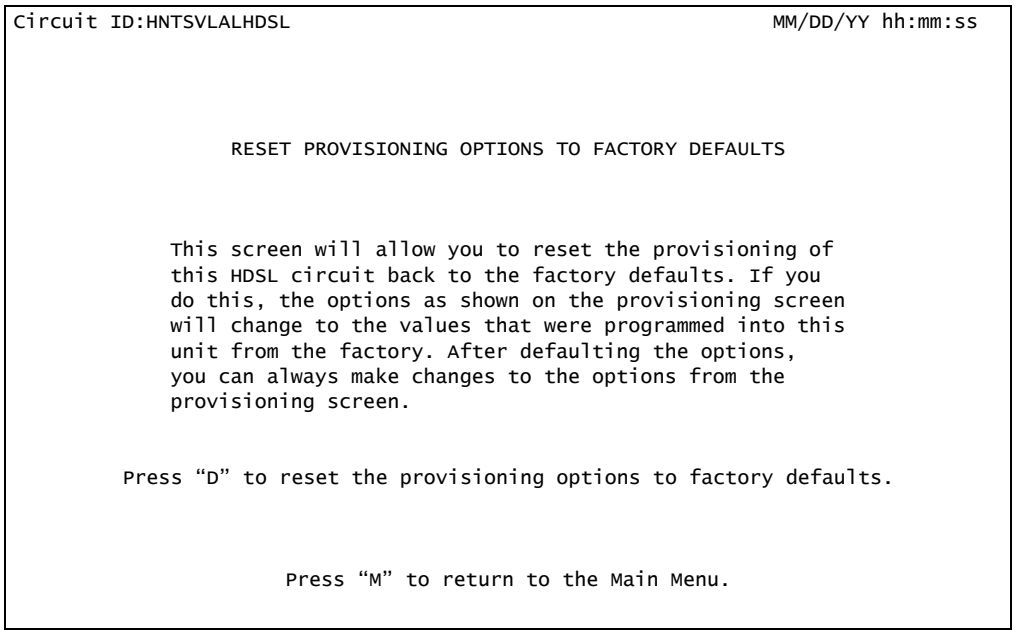

Figure 18. Default Options Screen

HDSL SYSTEM TESTING

The ADTRAN HDSL system provides the ability to monitor the status and performance of the DSX-1 signals, DS1 signals, and HDSL loop signals. Detailed performance monitoring is provisioned by the faceplate-mounted RS-232 Control Port. These features are valuable in troubleshooting and isolating any system level problems that may occur at installation or during operation of the HDSL system. The following paragraphs describe additional testing features.

3192 HTU-C DSX Bantam Jacks

The front panel of the 3192 HTU-C contains metallic splitting bantam jacks for both nonintrusive (monitoring) and intrusive (terminating) DSX-1 test access. Refer to Figure 19 for details for specific jacks.

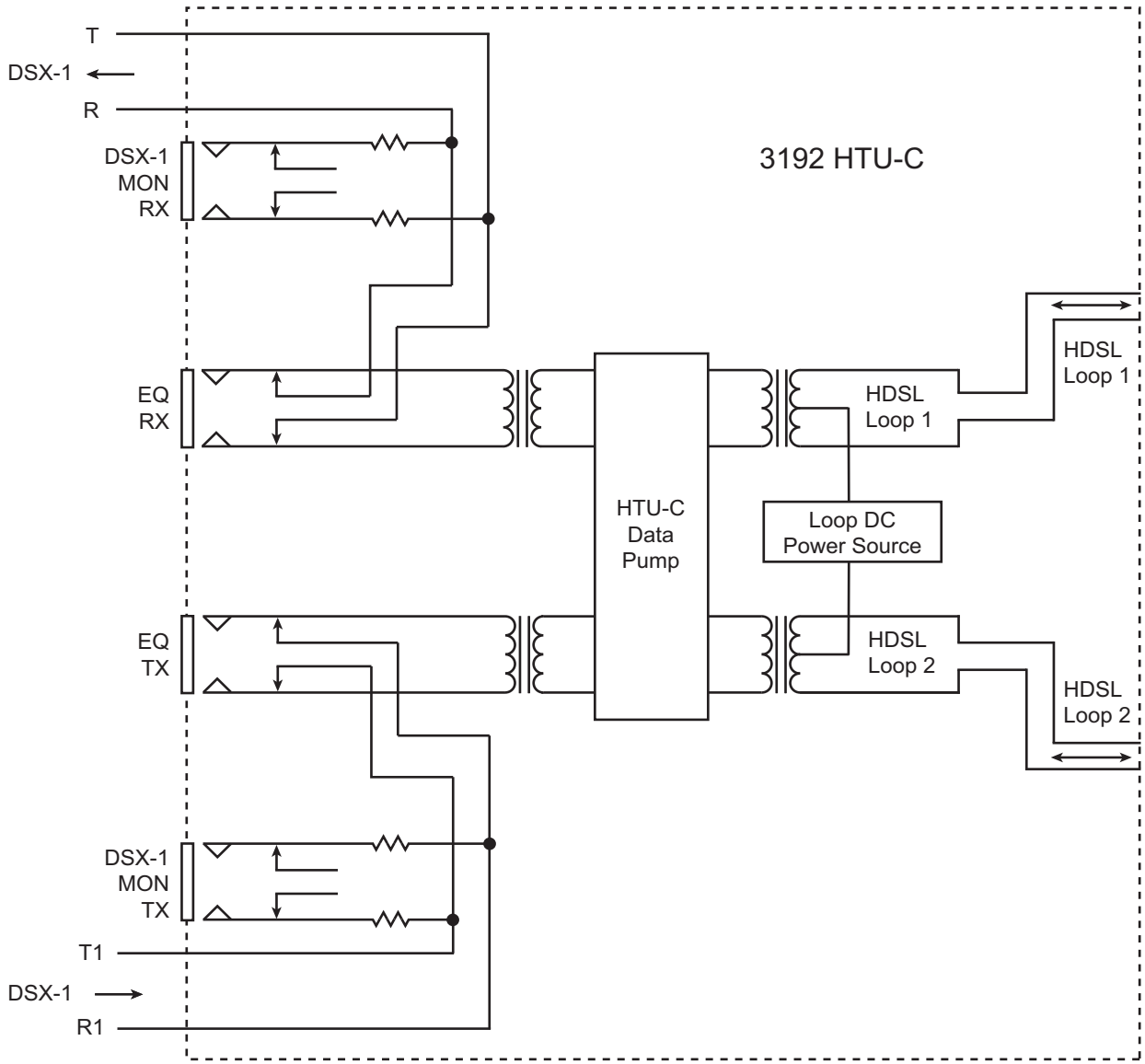

Figure 19. 3192 HTU-C Bantam Jack Arrangement

3192 HTU-C Loopbacks

The 3192 HTU-C responds to two different loopback activation processes. First, loopbacks may be commanded manually using the control port interface. Table 12 depicts the Loopback Options Screen which provides for 3192 HTU-C, HTU-R, and HRE loopbacks.

Second, the 3192 HTU-C responds to the industry standard sequences for HDSL loopbacks. These loopback sequences are described in detail in "Appendix A, HDSL Loopbacks" of this practice.

The loopback condition imposed in both cases is a logic level loopback at the point within the 3192 HTU-C where the DSX-1 signal passes into the HDSL modulators. Figure 20 depicts all the loopback locations possible with ADTRAN HDSL equipment.

In addition to network-side loopbacks, the 3192 HTU-C provides customer-side loopbacks initiated by using the terminal control port. In this mode, an AIS signal is supplied to the network. Customer-side loopbacks must be deactivated using the terminal.

NOTE

When the 11000 in-band code is used to loop the HTU-R, the HTU-R re-enters armed state upon loopback timeout, or upon terminal deactivation. Refer to "Appendix A, HDSL Loopbacks" for a more detailed description of loopback element states.

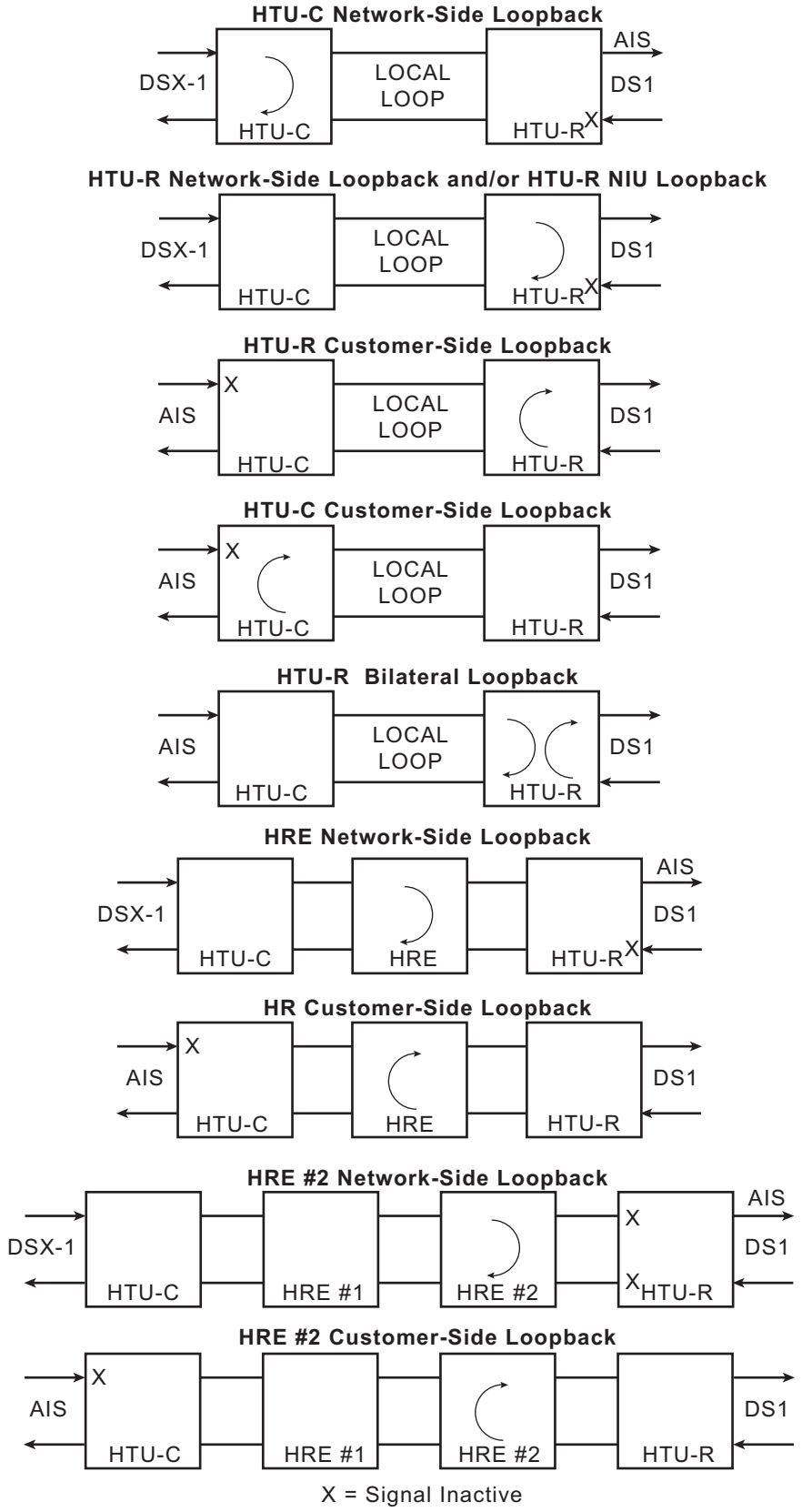

Figure 20. HDSL Loopbacks

TROUBLESHOOTING PROCEDURES

Table 15 is a troubleshooting guide for the ADTRAN 3192 HTU-C.

Table 15. 3192 HTU-C Troubleshooting Guide

Condition	Solution
Front panel indicator is *off*.	1. Verify that –48 VDC power is properly connected to the shelf. 2. Inspect the fuse (F1) and verify that it is not blown. 3. Insert the 3192 HTU-C into a slot known to be in good working condition; check to see if the STATUS indicator is on. 4. If Steps 1 and 2 pass, but Step 3 fails, replace the 3192 HTU-C.

MAINTENANCE

The 3192 HTU-C requires no routine maintenance. In case of equipment malfunction, use the faceplate-mounted DB9 RS-232 terminal interface to help in troubleshooting the source of the problem.

ADTRAN does not recommend that repairs be performed in the field. Repair services may be obtained by returning the defective 3192 HTU-C to the ADTRAN Customer Service RMA Department.

SPECIFICATIONS

The 3192 HTU-C specifications are detailed in Table 16.

Table 16. 3192 HTU-C Specifications

Specification	Description
Loop Interface:	
Modulation Type:	2B1Q
Mode:	Full Duplex; Echo Cancelling
Number of Pairs:	Two
Bit Rate:	784 kbps per pair
Baud Rate:	392 k baud per pair
Service Range:	Defined by CSA Guidelines
Loop Loss:	35 dB maximum at 196 kHz
Bridged Taps:	Single Taps < 2000 feet; Total Taps < 2500 feet
Performance:	Compliant with Bellcore TA-NWT-001210
HDSL Tx Signal Level:	13.5 dBm
Input Impedance:	135 ohms
Return Loss:	20 dB (40 kHz to 200 kHz)
Network Interface: 4-WIRE DSX-1	
DSX-1 Output Level:	0 dB
DSX-1 Line Build Out:	0–133 feet; 133–266 feet; 266–399 feet; 399–533 feet; 533–655 feet;
DSX-1 Line Code:	AMI B8ZS
DSX-1 Format:	SF; ESF; Unframed
DSX-1 Channelization:	Channels 1–12 on HDSL Loop 1; Channels 13–24 on HDSL Loop 2

Table 16. 3192 HTU-C Specifications (Continued)

Specification	Description
Power: Tested with the ADTRAN HRE (P/N 1247045L1) and HTU-R (P/N 1247026L1)	
Total Power:	–48 VDC at 200 mA with HTU-R; –48 VDC at 330 mA with HTU-R and one HRE; –48 VDC at 560 mA with HTU-R and two HREs
3192 HTU-C Power Dissipation:	5.1 watts with HTU-R; 5.7 watts with one HRE and HTU-R; 7.1 watts with two HREs and HTU-R
Span Power:	–190 VDC (internally generated): Class A2 compliant; Current limit at 150 mA
Fusing:	1.00 amp (on-board; not field replaceable)
Clock:	
Clock Sources:	Internal, DSX-1 Derived
Internal Clock Accuracy:	±25 ppm, (exceeds Stratum 4). Meets T1.101 timing requirements.
Tests:	
Diagnostics:	Self-Test; Loopbacks: • Local (HTU-C) • Remote (HTU-R) • HRE
Physical:	
Mounting:	CI Wescom Office Repeater Shelf-Mounted
Dimensions:	Height: 4.75 inches; Width: 0.69 inches; Depth: 10.5 inches
Weight:	Less than 1 pound
Environment:	
Temperature:	Operating (Standard): –40°C to +70°C; Storage: –40°C to +85°C
Control Port:	
Interface:	RS-232 (DB9)
Terminal Type:	VT-100 or compatible
Async Speed:	2.4 kbps to 19.2 kbps
Data Format:	• 8 data bits • No parity • 1 stop bit
Part Number:	
3192 HTU-C:	1247004L1

Appendix A
HDSL Loopbacks

HDSL MAINTENANCE MODES

This Appendix describes operation of the HDSL system with regard to detection of in-band and ESF facility data link loopback codes. The operation of the loopback commands in the ADTRAN HDSL system is compliant with the recommendation to ANSI recorded in T1E1.4/92. The HDSL network loopback points described below are shown in Figure A-1.

3192 HTU-C Loopback

A regenerative loopback of the DSX-1 signal toward the network.

HTU-R Loopback

A regenerative loopback of the DS1 signal toward the network. This loopback is in addition to a separate Smartjack loopback. Separate activation sequences are provided for the HTU-R and the Smartjack loopback initiation.

HRE Loopback

A regenerative loopback of the HDSL signal toward the network.

Upon deactivation of a loopback, the HDSL system synchronizes automatically. Note that the synchronization process of the HDSL system upon deactivation of the HRE loopback could take up to 15 seconds, ensuring all system elements are synchronized.

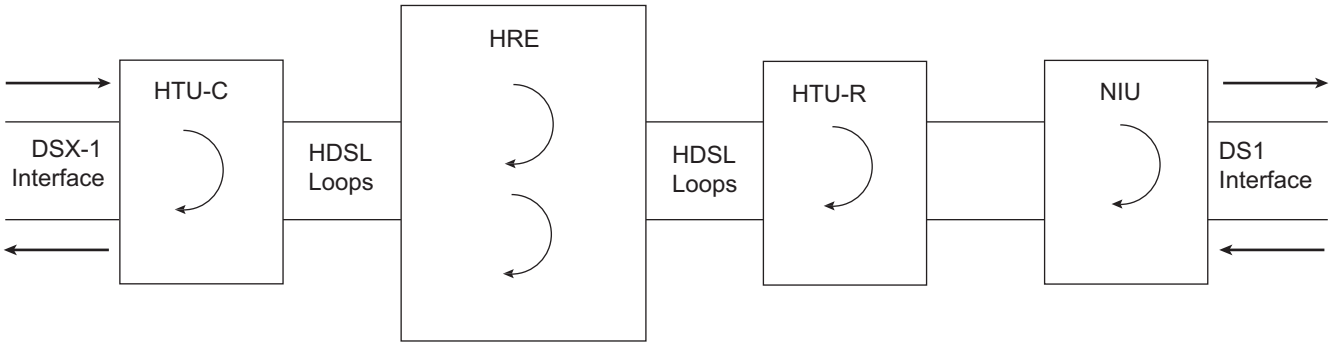

Figure A-1. HDSL Loopback Points

Loopback Process Description

In general, the loopback process for the HDSL system elements is modeled on the corresponding DS1 system process. Specifically, the 3192 HTU-C loopback is similar to an Intelligent Office Repeater loopback and the HTU-R loopbacks are similar to an inline T1 Repeater loopback.

Each HDSL system element is independently described by the state diagram shown in Figure A-2. The four states are disarmed, loop-up, armed, and loop-up/time-out disable.

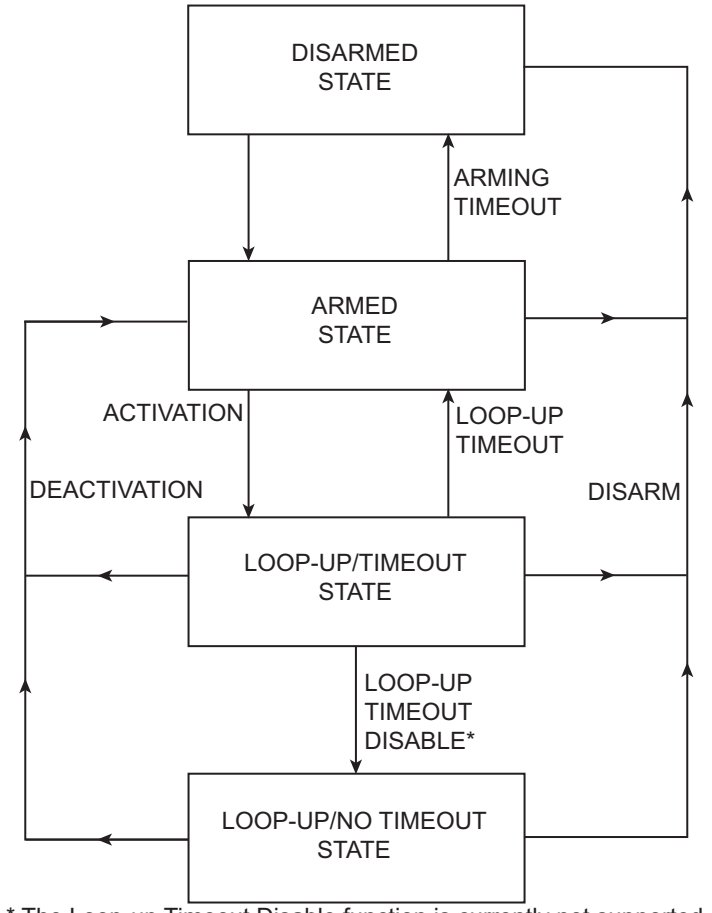

Figure A-2. HDSL Element State Diagram

State transitions result from in-band and ESF Data Link sequences as well as timeout operations. The sequences and timeout values are as follows:

- Arming Sequence (in-band and ESF)
- Activation Sequence
- Deactivation Sequence
- Disarming Sequence (in-band and ESF)
- Loop-up Timeout
- Arming Timeout

In-band control code sequences are transmitted over the DS1 link by either the unframed or overwrite method. The HDSL elements respond to either method.

The unframed method produces periodic control sequences and the normal DS1 framing bit is omitted.

The overwrite method produces periodic control sequences. However, once per frame, the framing bit overwrites one of the bits in the control sequence.

The unit can detect the loopback activation or deactivation code sequence only if an error rate of 1E-03 or better is present.

HDSL Loopback Codes

A summary of HDSL loopback codes are given in Table A-1.

Table A-1. HDSL Loopback Control Codes

Function	Code (Binary/Hex)	Response
Arming (In-band)	11000	Signal sent in-band or over ESF data link.
Arming (ESF)	1111 1111 0100 1000	HDSL elements in disarmed state make transition to armed state. Detection of either code results in Smartjack loop up, if NIU loopback is enabled.
Activation (HTU-C)	1101 0011 1101 0011	Signal sent in-band. HDSL elements in armed state make transition to loop up state. Loop up state time out is programmable from the HTU-C. Sends 231 bit errors every 20 seconds.
Arm Source: Network	100000 (1 in 6)	Signal sent in-band. HDSL elements in disarmed state transition to armed state. Detection of the code results in an HTUR network loopback if New England Loopback (NELB) is enabled.
Disarming (In-band)	11100	Signal sent in-band. HDSL elements loopdown and transition to disarmed state.
Disarming (ESF)	1111 1111 0010 0100	Signal sent in the FDL. HDSL elements loopdown and transition to disarmed state.
Deactivation	1001 0011 1001 0011	Signal sent in-band. HDSL elements loopdown and transition to disarmed state. If NIU is enabled, the HTU-R does not loopdown.
Disarm Source: Network or Customer	100 (1 in 3)	Signal sent in-band. HDSL elements loopdown and transition to disarmed state.
Activation (HTU-C) Source: Network	1111 1111 0001 1110/ FF1E	Signal sent in-band. HTU-C loops back the T1 data to the network equipment.
Activation (HTU-C) Source: Network	1111000 (4 in 7)	Signal sent in-band. HTU-C loops back the T1 data to the network equipment.

Table A-1. HDSL Loopback Control Codes (Continued)

Function	Code (Binary/Hex)	Response
Activation (HTU-C) Source: Customer	1111110 (6 in 7)	Signal sent in-band. HTU-C loops back the T1 data to the customer equipment.
Activation (HTU-C) Source: Customer	0111 1111 0001 1110/ 3F1E	Signal sent in-band. HTU-C loops back the T1 data to the customer equipment.
Activation (HTU-R) Source: Network	1111 1111 0000 0010/ FF02	Signal sent in-band. HTU-R loops back the T1 data to the network equipment.
Activation (HTU-R) Source: Network	1110000 (3 in 7)	Signal sent in-band. HTU-R loops back the T1 data to the network equipment.
Activation (HTU-R) Source: Customer	1111100 (5 in 7)	Signal sent in-band. HTU-R loops back the T1 data to the customer equipment.
Activation (HTU-R) Source: Customer	0011 1111 0000 0010/ 3F02	Signal sent in-band. HTU-R loops back the T1 data to the customer equipment.
Activation (HTU-R) Source: Network or Customer	1100 0111 0100 0010/ C742	Signal sent in-band. HTU-R loops back the T1 data to the source direction. Sends 20 bit errors every 10 seconds.
Activation (HRE-1) Source: Network or Customer	1100 0111 0100 0001/ C741	Signal sent in-band. HRE-1 loops back the T1 data to the source direction. Sends 10 bit errors every 20 seconds.
Activation (HRE-2) Source: Network or Customer	1100 0111 0101 0100/ C754	Signal sent in-band. HRE-2 loops back the T1 data to the source direction. Sends 200 bit errors every 20 seconds.
Query (All Elements) Source: Network	1101 0101 1101 0101/ D5D5	Signal sent in-band. Any unit that is in network loopback injects bit errors into the data looped to the network equipment. The element closest to the network that is in loopback injects the errors. • HTU-C that is in a network loopback injects 231 bit errors every 20 seconds. • HTU-R that is in a network loopback injects 20 bit errors every 10 seconds. • HRE-1 that is in a network loopback injects 10 bit errors every 20 seconds. • HRE-2 that is in a network loopback injects 200 bit errors every 20 seconds.
Disable Loopback Timeout Source: Network	1101 0101 1101 0110/ D5D6	Signal sent in-band. Loopback Timeout is disabled as long as any element is in loopback or armed.
Disable Span Power Source: Network	0110 0111 0110 0111/ 6767	Signal sent in-band. Span power is disabled until this pattern is removed.

Appendix B
DS0 Blocking

ADTRAN has implemented the DS0 blocking feature enabling the HDSL system to remain transparent to customer data. This allows ADTRAN products to comply with the transparency requirements of Bellcore TA-NWT-001210. However, when the circuit is provisioned for ESF operation, this transparency results in a condition described below.

If a customer of a Fractional T1 service fills any of the unused DS0 channels with information other than an all 1s idle code, the ADTRAN HDSL system blocks this information from reaching the remote end of the circuit. This forces information in those DS0 channels to be an all 1s idle code.

The result of this blocking is that the CRC checksum delivered to the remote end will not match the checksum calculated by the remote T1 CSU. This implies errors are being made on the loop when actually the blocking function created the CRC errors. Enabled DS0 channels pass error-free.

In order to avoid this condition, Fractional T1 customers are encouraged to fill the unused timeslots with an idle code. This is a common capability on Fractional T1 CSU/DSU, D4 channel banks, and other CPE devices capable of connecting to Fractional T1 service.

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Appendix C
Warranty

WARRANTY AND CUSTOMER SERVICE

ADTRAN will replace or repair this product within the warranty period if it does not meet its published specifications or fails while in service. Warranty information can be found at www.adtran.com/warranty.

Refer to the following subsections for sales, support, Customer and Product Service (CAPS) requests, or further information.

ADTRAN Sales

Pricing/Availability:

800-827-0807

ADTRAN Technical Support

Pre-Sales Applications/Post-Sales Technical Assistance:

800-726-8663

Standard hours: Monday - Friday, 7 a.m. - 7 p.m. CST
Emergency hours: 7 days/week, 24 hours/day

ADTRAN Repair/CAPS

Return for Repair/Upgrade:

(256) 963-8722

Repair and Return Address

Contact CAPS prior to returning equipment to ADTRAN.

ADTRAN, Inc.
CAPS Department
901 Explorer Boulevard
Huntsville, Alabama 35806-2807

Carrier Networks Division
901 Explorer Blvd.
Huntsville, AL 35806